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 Kandemir, M.; Choudhary, A.; Ramanujam, J.; Banerjee, P.;
Parallel and Distributed Systems, IEEE Transactions on
 Volume 14, Issue 4, April 2003 Page(s):337 - 354
 Digital Object Identifier 10.1109/TPDS.2003.1195407
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2. **Simulation study of memory performance of SMP multiprocessors running workload**
 Foglia, P.; Giorgi, R.; Prete, C.A.;
Computers and Digital Techniques, IEE Proceedings-
 Volume 151, Issue 2, 19 March 2004 Page(s):93 - 109
 Digital Object Identifier 10.1049/ip-cdt:20040349
[AbstractPlus](#) | Full Text: [PDF\(1698 KB\)](#) IEE JNL

3. **False sharing and spatial locality in multiprocessor caches**
 Torrellas, J.; Lam, H.S.; Hennessy, J.L.;
Computers, IEEE Transactions on
 Volume 43, Issue 6, June 1994 Page(s):651 - 663
 Digital Object Identifier 10.1109/12.286299
[AbstractPlus](#) | Full Text: [PDF\(1284 KB\)](#) IEEE JNL
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4. **On reducing false sharing while improving locality on shared memory multiprocessors**
 Kandemir, M.; Choudhary, A.; Ramanujam, J.; Banerjee, P.;
Parallel Architectures and Compilation Techniques, 1999. Proceedings. 1999 International Conference on
 12-16 Oct. 1999 Page(s):203 - 211
 Digital Object Identifier 10.1109/PACT.1999.807529
[AbstractPlus](#) | Full Text: [PDF\(244 KB\)](#) IEEE CNF
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5. **On the effectiveness of sectored caches in reducing false sharing misses**
 Kuang-Chih Liu; Chung-Ta King;
Parallel and Distributed Systems, 1997. Proceedings., 1997 International Conference on
 10-13 Dec. 1997 Page(s):352 - 359
 Digital Object Identifier 10.1109/ICPADS.1997.652573

[AbstractPlus](#) | Full Text: [PDF\(632 KB\)](#) IEEE CNF
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- 6. **An effective shared memory allocator for reducing false sharing in NUMA multiprocessors**
Jong Woo Lee; Yookun Cho;
[Algorithms and Architectures for Parallel Processing, 1996. ICAPP '96. 1996 International Conference on](#)
11-13 June 1996 Page(s):373 - 382
Digital Object Identifier 10.1109/ICAPP.1996.562898
[AbstractPlus](#) | Full Text: [PDF\(904 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- 7. **Partitioning regular grid applications with irregular boundaries for cache-multiprocessors**
Yang Zeng; Abraham, S.G.;
[Parallel Processing Symposium, 1995. Proceedings., 9th International](#)
25-28 April 1995 Page(s):222 - 228
Digital Object Identifier 10.1109/IPPS.1995.395936
[AbstractPlus](#) | Full Text: [PDF\(860 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- 8. **False sharing elimination by selection of runtime scheduling parameters**
Jyh-Herng Chow; Sarkar, V.;
[Parallel Processing, 1997.. Proceedings of the 1997 International Conference](#)
11-15 Aug. 1997 Page(s):396 - 403
Digital Object Identifier 10.1109/ICPP.1997.622672
[AbstractPlus](#) | Full Text: [PDF\(664 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- 9. **Reduction of false sharing by using process affinity in page-based distributed memory multiprocessor systems**
Hung, K.P.; Yung, N.H.C.; Cheung, Y.S.;
[Algorithms and Architectures for Parallel Processing, 1996. ICAPP '96. 1996 International Conference on](#)
11-13 June 1996 Page(s):383 - 390
Digital Object Identifier 10.1109/ICAPP.1996.562899
[AbstractPlus](#) | Full Text: [PDF\(848 KB\)](#) IEEE CNF
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Saha, A.; Lin, J.; Lockett, C.; Malik, N.; Shamsi, U.;
[Computers and Communications, 1996.. Conference Proceedings of the 1996 Annual International Phoenix Conference on](#)
27-29 March 1996 Page(s):373 - 377
Digital Object Identifier 10.1109/PCCC.1996.493659
[AbstractPlus](#) | Full Text: [PDF\(476 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- 11. **Performance analysis of electronic commerce multiprocessor server**
Foglia, P.; Giorgi, R.; Prete, C.A.;
[System Sciences, 2000. Proceedings of the 33rd Annual Hawaii International](#)
Jan 4-7 2000 Page(s):9 pp.
[AbstractPlus](#) | Full Text: [PDF\(120 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- 12. **The impact of parallel loop scheduling strategies on prefetching in a shared multiprocessor**
Lilja, D.J.;

[Parallel and Distributed Systems, IEEE Transactions on](#)
Volume 5, Issue 6, June 1994 Page(s):573 - 584
Digital Object Identifier 10.1109/71.285604
[AbstractPlus | Full Text: PDF\(1288 KB\) IEEE JNL](#)
[Rights and Permissions](#)

- **13. A New Technique to Reduce False Sharing in Parallel Irregular Codes Ba Functions**
Pichel, J.C.; Heras, D.B.; Cabaleiro, J.C.; Rivera, F.F.;
[Parallel Architectures, Algorithms and Networks, 2005. ISSPAN 2005. Proceedir International Symposium on](#)
07-09 Dec. 2005 Page(s):306 - 311
Digital Object Identifier 10.1109/ISSPAN.2005.10
[AbstractPlus | Full Text: PDF\(264 KB\) IEEE CNF](#)
[Rights and Permissions](#)
- **14. An effective full-map directory scheme for the sectored caches**
Won-Kee Hong; Tack-Don Han; Shin-Dug Kim; Sung-Bong Yang;
[High Performance Computing on the Information Superhighway, 1997. HPC A](#)
28 April-2 May 1997 Page(s):7 - 11
Digital Object Identifier 10.1109/HPC.1997.592113
[AbstractPlus | Full Text: PDF\(460 KB\) IEEE CNF](#)
[Rights and Permissions](#)
- **15. Software caching on cache-coherent multiprocessors**
Bianchini, R.; LeBlanc, T.J.;
[Parallel and Distributed Processing, 1992. Proceedings of the Fourth IEEE Sy](#)
1-4 Dec. 1992 Page(s):521 - 526
Digital Object Identifier 10.1109/SPDP.1992.242700
[AbstractPlus | Full Text: PDF\(540 KB\) IEEE CNF](#)
[Rights and Permissions](#)
- **16. Characterizing the memory behavior of compiler-parallelized application:**
Torrie, E.; Martonosi, M.; Chau-Wen Tseng; Hall, M.W.;
[Parallel and Distributed Systems, IEEE Transactions on](#)
Volume 7, Issue 12, Dec. 1996 Page(s):1224 - 1237
Digital Object Identifier 10.1109/71.553272
[AbstractPlus | References | Full Text: PDF\(2060 KB\) IEEE JNL](#)
[Rights and Permissions](#)
- **17. Miss penalty reduction using bundled capacity prefetching in multiproce**
Wallin, D.; Hagersten, E.;
[Parallel and Distributed Processing Symposium, 2003. Proceedings. Internat](#)
22-26 April 2003 Page(s):9 pp.
Digital Object Identifier 10.1109/IPDPS.2003.1213088
[AbstractPlus | Full Text: PDF\(869 KB\) IEEE CNF](#)
[Rights and Permissions](#)
- **18. On the value locality of store instructions**
Lepak, K.M.; Lipasti, M.H.;
[Computer Architecture, 2000. Proceedings of the 27th International Symposium](#)
2000 Page(s):182 - 191
[AbstractPlus | Full Text: PDF\(1036 KB\) IEEE CNF](#)
[Rights and Permissions](#)
- **19. Photonic architectures for distributed shared memory multiprocessors**
Dowd, P.W.; Chu, J.;
[Massively Parallel Processing Using Optical Interconnections, 1994., Proceedi](#)
[International Workshop on](#)

26-27 April 1994 Page(s):151 - 161
Digital Object Identifier 10.1109/MPPOI.1994.336630
[AbstractPlus](#) | [Full Text: PDF\(904 KB\)](#) IEEE CNF
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- **20. Silent stores and store value locality**
Lepak, K.M.; Bell, G.B.; Lipasti, M.H.;
[Computers, IEEE Transactions on](#)
Volume 50, Issue 11, Nov. 2001 Page(s):1174 - 1190
Digital Object Identifier 10.1109/12.966493
[AbstractPlus](#) | [References](#) | [Full Text: PDF\(4282 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- **21. Analysis of shared memory misses and reference patterns**
Rothman, J.B.; Smith, A.J.;
[Computer Design, 2000. Proceedings. 2000 International Conference on](#)
17-20 Sept. 2000 Page(s):187 - 198
Digital Object Identifier 10.1109/ICCD.2000.878285
[AbstractPlus](#) | [Full Text: PDF\(1016 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- **22. Design and performance evaluation of an adaptive cache coherence protocol**
Won-Kee Hong; Nam-Hee Kim; Shin-Dug Kim;
[Parallel and Distributed Systems, 1998. Proceedings., 1998 International Conference on](#)
14-16 Dec. 1998 Page(s):33 - 40
Digital Object Identifier 10.1109/ICPADS.1998.741017
[AbstractPlus](#) | [Full Text: PDF\(152 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- **23. Distributed array data management on NUMA multiprocessors**
Abdelrahman, T.S.; Wong, T.N.;
[Scalable High-Performance Computing Conference, 1994. Proceedings of the](#)
23-25 May 1994 Page(s):551 - 559
Digital Object Identifier 10.1109/SHPCC.1994.296690
[AbstractPlus](#) | [Full Text: PDF\(616 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- **24. Automatic partitioning of data and computations on scalable shared memory multiprocessors**
Tandri, S.; Abdelrahman, T.S.;
[Parallel Processing, 1997., Proceedings of the 1997 International Conference](#)
11-15 Aug. 1997 Page(s):64 - 73
Digital Object Identifier 10.1109/ICPP.1997.622557
[AbstractPlus](#) | [Full Text: PDF\(976 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- **25. Two techniques for improving performance on bus-based multiprocessors**
Anderson, C.; Baer, J.-L.;
[High-Performance Computer Architecture, 1995. Proceedings. First IEEE Symposium on](#)
22-25 Jan. 1995 Page(s):264 - 275
Digital Object Identifier 10.1109/HPCA.1995.386536
[AbstractPlus](#) | [Full Text: PDF\(668 KB\)](#) IEEE CNF
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26. Improving locality using loop and data transformations in an integrated f
 Kandemir, M.; Choudhary, A.; Ramanujam, J.; Banerjee, P.;
Microarchitecture, 1998. MICRO-31. Proceedings. 31st Annual ACM/IEEE Inte
Symposium on
 30 Nov.-2 Dec. 1998 Page(s):285 - 296
 Digital Object Identifier 10.1109/MICRO.1998.742790
[AbstractPlus](#) | Full Text: [PDF\(260 KB\)](#) [IEEE CNF](#)
[Rights and Permissions](#)

27. The performance impact of false subpage sharing in KSR1
 Cukic, B.; Bastani, F.B.;
Frontiers of Massively Parallel Computation, 1995. Proceedings. 'Frontiers '95
Symposium on the
 6-9 Feb. 1995 Page(s):64 - 71
 Digital Object Identifier 10.1109/FMPC.1995.380462
[AbstractPlus](#) | Full Text: [PDF\(560 KB\)](#) [IEEE CNF](#)
[Rights and Permissions](#)

28. An empirical evaluation of two memory-efficient directory methods
 O'Kafka, B.W.; Newton, A.R.;
Computer Architecture, 1990. Proceedings. 17th Annual International Sympos
 28-31 May 1990 Page(s):138 - 147
 Digital Object Identifier 10.1109/ISCA.1990.134519
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